

Claims 1 through 8 have been rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. published patent application 2002/0038393 (Ganapathy). The rejection is respectfully traversed. Claim 1, the only independent claim, is reproduced as follows:

1. A direct memory access controller, comprising:
a plurality of direct memory access transfer portions
controlling direct memory access transfer in accordance with
values set in a group of registers for current transfer; and
a control portion permitting use of a bus by said
plurality of direct memory access transfer portions in a prescribed
order such that bus ownership is passed among channels at every
prescribed number of transfers, in response to transfer requests
from said plurality of direct memory access transfer portions, while
the bus ownership is granted from a bus master.

As required by claim 1, a control portion in a direct memory access controller permits use of a bus by a plurality of direct memory access transfer portions in a prescribed order such that bus ownership is passed among channels at every prescribed number of transfers, in response to transfer requests from the plurality of direct memory access transfer portions, while the bus ownership is granted from a bus master. It is noted, in particular, that claim 1 requires that bus ownership is passed *among channels in the direct memory access controller* and that the bus ownership is passed *at every prescribed number of transfers*.

It is submitted that Ganapathy does not disclose these claimed requirements and thus does not support a rejection for anticipation within the purview of 35 U.S.C. § 102. Ganapathy discloses that “a round-robin arbitration scheme on the system bus 200 assures that each of the distributed DMA master controllers 203A-203N, 207 and 215 have access every so often to the system bus 200 and can access the global memory 210 at that time (see paragraph 0019).” Ganapathy does not disclose passing bus ownership among channels in the direct memory access controller. Ganapathy does not disclose that bus ownership is passed at every prescribed number of transfers.

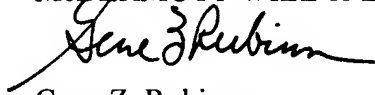
Application No.: 10/821,985

It is submitted, therefore, that claim 1 recites subject matter that is not met by the disclosure of Ganapathy. Claims 2 through 8, which depend from claim 1, are distinguishable from Ganapathy for at least the same reasons.

Withdrawal of the rejection and allowance of the application are respectfully solicited. To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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